Alex Pacini

Principal Power Electronics Engineer
PhD Electrical Engineering

alexpacini.com
alexpacini90@gmail.com
Villach, 9500, AT



Power Electronics · GaN/SiC (WBG) · Cross-Domain R&D Leadership
Multi-Objective Optimisation · System Modelling · AI Automation

Summary

R&D and innovation leader with a PhD in electrical engineering and over a decade of experience bridging novel electronics systems, high-leverage data-driven automation, and AI-augmented engineering workflows. Core expertise spans multi-objective optimisation (Pareto/MOO) and system-level modelling, simulation, and trade-offs for next-generation GaN/SiC power systems, with a cross-domain background in power electronics, RF/microwaves, embedded systems, and software design. Has invented and patented novel converter concepts (9 patent families), defined strategic technology roadmaps, and led complex projects and cross-functional teams from feasibility through to deployed systems, with quick time-to-market and business impact. Driven by intellectual curiosity, first-principles thinking, a commitment to open collaboration, and sustainable innovation.

Expertise

Leadership	Team and project leadership (cross-functional, multi-site), Technical mentoring, Customer and stakeholder consulting, Technology roadmaps and scouting, IP strategy and patents, University and start-up collaboration.
Power & RF	Power Electronics, GaN/SiC (WBG), RF/Microwave, EMC/EMI, Wireless Power Transfer (WPT), High-Density Converters.
Design & Optimisation	Pareto Analysis (MOO), System-Level Trade-offs, Feasibility & Cost-Benefit, Abstraction & Generalisation, Digital Twin, Data-Driven R&D, Data Science, LLM-Augmented Engineering, AI-Assisted Tool Design, System Modelling & Simulation, and Control.
HW Design & Lab	PCB Design (KiCad, Altium), FPGA, 3D Printing, VNA, Power and RF Lab Testing, FreeCAD, Blender, Inkscape.
Simulation	PLECS, SPICE (ngspice, Xyce, LTspice, SIMetrix), Modelica (OpenModelica), Harmonic Balance, Ansys, ADS, CST Microwave Studio, GeckoCircuits, FMI/FMU.
Software	Python, Kotlin, Java, C, SQL, SystemVerilog, VHDL, Lua, AWK, LaTeX, RegEx, FreeRTOS, MATLAB, KNIME.
Infrastructure	Linux, Git, SVN, CI/CD and TDD (GitLab CI, Jenkins, pytest), Docker, Gradle, Artifactory, make/CMake, gRPC, ZeroMQ, Protobuf, Jira.
Languages	Italian (Native), English (Full Working Proficiency), German (Basic A1).

Work Experience

04/22 – present **Principal Power Electronics Innovation Engineer, Infineon Technologies AG, Villach, AT**

Leading strategic innovation projects, from first-principles analysis and proof of concept to customer engagement and IP, to guide decisions and demonstrate the value of novel, non-incremental technologies. IP creation and strategy, strategic tools development, AI-assisted workflows, and next-generation power systems design. Leading cross-functional teams and mentoring engineers and students through flexible, on-demand collaboration models. Providing internal and external consulting, including on-site support to automotive OEMs and Tier-1 suppliers on novel topologies and WBG technologies. Defining technology roadmaps, scouting emerging technologies, building collaborations with leading universities, and evaluating start-ups for strategic partnerships.

- Design of an 11kW true 3-phase single-stage resonant LLC-like cyclo-converter with bidirectional GaN switches for 800V server PSUs. Novel ZVS control scheme with low computation requirement which can be implemented on an MCU [17, 20, 23, 30, 33, 38, 41].
- System-level modelling and control design of an active pulsation buffer for 800V DC AI data-centre power distribution in Modelica (OpenModelica), including linearisation and RHP-zero analysis, current-control design, virtual damping, and DC bus impedance-based stability analysis.
- Led the architecture and development of a cross-platform Multi-Objective Optimisation (MOO) framework in Python, Kotlin, and SQL for power systems Pareto optimisation, covering magnetics, EMI filters, capacitors, switches, topologies, controls and modulations. Adopted for system-level large-scale DOE, portfolio rationalisation, and strategic technology selection (Si vs. SiC vs. GaN). Re-engineered the Pareto core for over 10× speed and 5× lower memory use, with CI/CD, automated tests and documentation deployment. Trained multiple internal teams and external partners on deployment and methodology. AI-friendly API design enabling sub-hour onboarding.
- Development of a multithreaded cross-platform Python interface to Infineon's in-house SPICE solver (powering InfineonSpice and IPOSIM SPICE). Runs thousands of simulations in parallel with significant licence cost savings and performance advantages over proprietary tools.
- Development of a SPICE-to-PLECS automated model generator supporting half-bridge, T-type and other topologies. Self-initiated bottom-up project, adopted as the official Infineon PLECS model generator and powering IPOSIM SPICE-in-the-loop simulations. Over 40% of new design wins leveraged the tool.
- Feasibility study of a non-isolated OBC for a major European automotive OEM. Analysis of an 11kW 3-phase T-type PFC/inverter and a 3-level flying-capacitor PFC/inverter under multiple required operating conditions. Developed novel modulation schemes to reduce switching losses and common mode current [5, 6, 7, 24, 27, 36].
- HW design of a calorimetric switching and hysteresis losses characterisation board and low parasitic capacitance floating driver PSU. Also designed the measurement automation and power control software, enabling first-time characterisation of dynamic C_{oss} losses for GaN devices [4].
- Development of AI-augmented engineering workflows with LLM-based agents grounded by self-developed data tools (including a Python patent intelligence tool for the EPO OPS API with automated reports) and MCP tool integrations, used for prior-art analysis, literature review, and software development. Refactored the optimisation framework for use with AI agents (API documentation, agent instructions, examples), enabling AI-assisted creation of new topology harnesses and configurations.

11/20 – 03/22 **Staff Product Definition Engineer, Infineon Technologies AG, Villach, AT**

Product Definition Engineer for 650V GaN power switches, providing technical leadership to four engineers, including task planning and technical decisions.

- Definition of device parameters, portfolio, requirements, and evaluation of the second generation Infineon GaN GIT devices (released in 2024 as G5).
- HW design of a 3.3kW single phase Totem Pole PFC and a 3.3kW HB LLC as fair universal benchmarking platforms for different devices, packages and driving schemes.
- Design of a Python tool for measurement post-processing, data versioning and automatic reporting. Reduced manual processing time from days to less than an hour.
- GaN and SiC design consulting, e.g. gate driving and critical-path layout.

11/18 – 10/20 **Electronics Engineering Team Lead, Metaboards, Oxford, UK**

Metaboards is a spin-out from Oxford University focused on metamaterials for inductive power transfer. Leading a four-person electronics team, responsible for the R&D and project definition of power and RF hardware solutions, including feasibility study, specs, design, simulations, testing, failure analysis, and documentation.

- HW/SW design of an NFC (13.56MHz) SIMO wireless data and charging solution, delivering 5W to 3 receivers with 65% system efficiency.

- Design of a 50W, 6.78MHz AirFuel-based Si Class E inverter and optimised matching network for extended load compression and 93% DC-RF efficiency.
- Design of an Automatic Tuning Unit to match the Class E to impedances from 2 to $300\,\Omega$. The system can also be used for real-time impedance measurement or load-pull measurements.
- Design of an 8-bit DSO-based nonlinear impedance measurement system using a custom current transformer and calibration algorithm for a $< \pm 2\%$ error up to $30\,\mathrm{MHz}$ and up to 50W capability.
- Design of a cycle-by-cycle current/voltage-limited $100\,\mathrm{W}$ Qi Si inverter with 97% efficiency at $200\,\mathrm{kHz}$ and operation up to $1\,\mathrm{MHz}$.
- Design of a "tandem" directional coupler, with $34\,\mathrm{dB}$ coupling and over $40\,\mathrm{dB}$ directivity, for impedance measurements up to $1\,\mathrm{MHz}$.
- Design of a Python library to run parametric Harmonic Balance simulations using Xyce.

04/18 – 07/18 **Visiting PhD Student – Imperial College, London, UK**, *"Position Readout by Current Sensing in a Position and Load Independent Sliding IPT"*, Control and Power Research Group, Prof. Paul Mitcheson

Designed, built and measured a transmitter side and feedback-less receiver position sensing mechanism with sub-cm accuracy for IPT applications. See my PhD thesis for more details.

03/18 – 04/18 **Contract Researcher – Alma Mater Studiorum, Università di Bologna**, *"Experimental Characterization of an Inductive Link between Planar Coils"*

Characterisation of quality factors and coupling coefficients for different distances between coils. This was performed with a VNA and custom post-processing Python scripts.

12/17 – 01/18 **Contract Researcher – Alma Mater Studiorum, Università di Bologna**, *"Experimental Characterization of a Wearable RFID Tag at 2.45GHz"*

Antenna Gain, Link Budget and EMI measurements.

05/17 **Visiting PhD Student – Imperial College, London, UK**, *"Measurements of a Position-Independent Sliding IPT"*, Control and Power Research Group, Prof. Paul Mitcheson, Supported by COST IC1301 (WiPE)

The multi-coil system designed in the previous STSM has been built, optimised and characterised. The total measured dc-dc efficiency at $100\,\mathrm{W}$ is 83%, independent of receiver's position.

02/17 – 09/18 **Teaching Assistant – Alma Mater Studiorum, Università di Bologna, Forlì**, *"Elettrotecnica (Circuit Theory)"*, Prof. Franco Mastri

Assistant for all the tasks related to lectures, from classroom exercises to exam corrections. Designed a LaTeX template for exam typesetting (cesenaexam on CTAN).

07/16 – 09/16 **Visiting PhD Student – Imperial College, London, UK**, *"Position-Independent Sliding Inductive WREL"*, Control and Power Research Group, Prof. Paul Mitcheson, Supported by COST IC1301 (WiPE)

Designed a $6.78\,\mathrm{MHz}$ position and load independent IPT system, composed of multiple transmitting constant current GaN Class EF inverters and a SiC Class E rectifier, for an output power of $100\,\mathrm{W}$. The work was completed in the following STSM.

06/15 – 09/15 **Contract Researcher – Alma Mater Studiorum, Università di Bologna, Cesena**, *"Design of UWB planar antennas on unconventional magneto-dielectric substrates"*

This work consisted in the development of a transmission line method for the characterisation of magneto-dielectric substrates and the design of an optimal antenna.

03/12 – 06/12 **Internship – Alma Mater Studiorum, Università di Bologna, Cesena**, *"Electromagnetic Characterization and Modelling of Magneto-Dielectric Composites"*

06/08 – 08/08 **BL Elettronica**, *Design, test and repair of PCB for industrial systems*

06/07 – 08/07 **BL Elettronica**, *Design, test and repair of PCB for industrial systems*

06/05 – 09/05 **Waeco Italcold**, *Production of portable refrigerators*

Education

2015 – 2019 **PhD in Electronics, Telecommunications, and Information Technologies Engineering**, *Alma Mater Studiorum - University of Bologna*, Cesena & Bologna, graduated in April 2019 with *Excellent, summa cum laude*

Thesis **Design of Novel Systems for Position Independent Energy and Data Transfer [43]**.
DOI: 10.6092/unibo/amsdottorato/8930.

During the PhD I developed a theoretical and experimental framework for the modelling, analysis and design of a complete position independent wireless energy and data transfer system for industrial applications. It provides a constant output dc voltage, without any feedback, while enabling passive receiver localisation. The link operates at 6.78MHz and is based on geometrically optimised coils, GaN Class EF inverters connected in *virtual series* and on a SiC Class E rectifier. It delivers over 100W with a dc-dc efficiency of 83% [1, 2, 9, 12, 13, 14, 15]. A data link with over 100Mb/s of bandwidth and high resilience to interference is integrated to provide a complete data and power transfer solution [8].
Additionally, I developed solutions for omnidirectional powering of miniaturised and loosely coupled ($< 1\%$) medical implants [10, 11].
Other responsibilities included co-supervising and mentoring final-year master's students, writing project deliverables and teaching assistance.

Awards *Design of a Position and Load Independent dc-to-dc Wireless Power Transfer System for Moving Applications*

- COST IC1301 Meeting 2017 Poster Session Winner, Porto, PT
- CPS 2017 Poster Session Winner, Cambridge, UK

Wireless Networks: From Energy Harvesting to Information Processing

- CTTC 2015 Summer School Student Design Competition Winner, Barcelona, Spain

2012 – 2015 **MS in Electrical and Telecommunication Engineering for Sustainable Development**, *Alma Mater Studiorum - University of Bologna*, Cesena, graduated in June 2015 with *110/110, summa cum laude*

Thesis **Approccio Teorico e Numerico per la Selezione di Topologie d'Antenna Miniaturizzate su Substrati Magneto-Dielettrici [44]**.

The thesis theoretically demonstrates the design rules to select the best magneto-dielectric material for a certain antenna topology, which provides the best compromise between miniaturisation, bandwidth and radiation efficiency [3, 16].

Awards *Wearable miniaturized magneto-dielectric antennas for Body Area Network and Wireless Power Transmission applications*

- MTT-S Undergraduate/Pre-graduate Scholarship for Fall 2013, IEEE Microwave Theory and Techniques Society, Tampa, FL, USA

Exams Autonomous RF Systems; Chemistry and Solid State Physics; Communications Systems; Electromagnetic Techniques for Localization and Environmental Control; Electronic Systems and Sensors; Electronics for information, energy generation and management; Information Processing Systems; Laboratory of Telecommunications; Advanced Mathematics and Physics; Multimedia Digital Signal Processing; Project in electronics and telecommunications; Propagation and Planning in Area Systems; Telecommunications Networks; Theory of Information and Codes; Wireless Sensor Networks for Environmental Monitoring.

2009 – 2012 **BS in Electrical, Computer Science and Telecommunication Engineering**, *Alma Mater Studiorum - University of Bologna*, Cesena, graduated in October 2012 with *110/110, summa cum laude*

Thesis **Studio di topologie d'antenne su substrati magneto-dielettrici [45]**.

The thesis studied the state-of-the-art of magneto-dielectric materials, and developed a transmission line theory to define new rules to select optimised antenna topologies.

Other activities included the position as a student representative for the degree programme.

Exams Advanced Topics in Electronics; Automatic Controls; Basic Principles of Chemistry; Digital Design Principles and Computer Architecture; Economics and Business Organisation; Electronic Measurements; Electronics; Electronics of Digital Systems; Electrotechnics; Foundations of Informatics A; General Physics A & B; Geometry and Algebra; Industrial Electronics; Laboratory of Telecommunications; Mathematical Analysis A & B; Microwaves; Propagation and Electromagnetic Fields; Signal Processing; Telecommunication techniques and applications; Telecommunications.

2004 – 2009 **Perito Capotecnico, Elettronica e Telecomunicazioni**, *ITIS Leonardo da Vinci*, Rimini

Awards *WAVE, Waves As Virtual Eyes*
This project consisted in an ultrasound extended reality belt aimed to assist and guide blind people.

- Expo Science Europe 2010, Moscow, Russia
- 2010 Intel Excellence in Computer Science Award
- I Giovani e le Scienze 2010, Milano, Italy
- Concorso CNA/CAR 2009, Rimini, Italy

Publications

alexpacini.com/pubs.html
0000-0002-0316-4989
scholar.google.com/citations?user=zBUuFHEAAAAJ

Journal Papers

[1] A. Pacini, F. Mastri, D. Masotti, and A. Costanzo, "Criticality Mitigation in a Quasi-Constant Coupling Position Independent Resonant IPT Network," *International Journal of Microwave and Wireless Technologies*, vol. 10, no. 8, pp. 911–920, Jun. 2018. DOI: 10.1017/S1759078718000788.

[2] A. Pacini, A. Costanzo, S. Aldhaher, and P. D. Mitcheson, "Load- and Position-Independent Moving MHz WPT System Based on GaN-Distributed Current Sources," *IEEE Transactions on Microwave Theory and Techniques*, vol. 65, no. 12, pp. 5367–5376, Dec. 2017. DOI: 10.1109/tmtt.2017.2768031.

[3] A. Pacini, A. Costanzo, and D. Masotti, "A Theoretical and Numerical Approach for Selecting Miniaturized Antenna Topologies on Magneto-Dielectric Substrates," *International Journal of Microwave and Wireless Technologies*, vol. 7, no. 3-4, pp. 369–377, May 2015. DOI: 10.1017/s1759078715000859.

Conference and Workshop Papers

[4] S. De Filippis, M. Kasper, A. Pacini, J. Sanz-Alcaine, and G. Deboy, "Experimental Characterization of Dynamic COSS Losses in 600 V GaN HEMTs based on a Novel and Simple Calorimetric Method," in *2024 IEEE Applied Power Electronics Conference and Exposition (APEC)*, Feb. 2024. DOI: 10.1109/APEC48139.2024.10509304.

[5] A. Pevere, M. Kasper, A. Pacini, G. Deboy, R. Petrella, and N. Rao, "Zero-Leakage-Current Zero-Common-Mode Modulation for Neutral-Point Connected Three-Phase Non-Isolated On-Board Chargers," in *2024 IEEE Applied Power Electronics Conference and Exposition (APEC)*, Feb. 2024. DOI: 10.1109/APEC48139.2024.10509213.

[6] A. Pacini, M. Kasper, A. Pevere, and G. Deboy, "Next Generation of High Power Density On-Board Chargers for Electric Vehicle Systems," in *2023 22nd International Symposium on Power Electronics (Ee)*, Oct. 2023. DOI: 10.1109/Ee59906.2023.10346098.

[7] M. Kasper, A. Pacini, A. Pevere, J. Anderson, and G. Deboy, "Concepts and Matching Power Semiconductor Devices for Compact On-Board Chargers," in *2023 IEEE 17th International Conference on Compatibility, Power Electronics and Power Engineering (CPE-POWERENG)*, Jun. 2023. DOI: 10.1109/cpe-powereng58103.2023.10227402.

[8] A. Pacini, F. Berra, D. Masotti, and A. Costanzo, "Uniform sliding system for Simultaneous WPT and Communication Data Transfer," in *2019 IEEE Radio and Wireless Symposium (RWS)*, Jan. 2019. DOI: 10.1109/rws.2019.8714337.

[9] A. Pacini, A. Costanzo, and D. Masotti, "Position-Independent Wireless Power Transfer in Sliding Inductive Links," in *2018 48th European Microwave Conference Workshop WF-06*, Institute of Electrical & Electronics Engineers (IEEE), Sep. 2018.

[10] A. Pacini, F. Benassi, D. Masotti, and A. Costanzo, "Design of a Miniaturized Omni-Directional RF-to-dc IR-WPT," in *2018 IEEE Wireless Power Transfer Conference (WPTC)*, Jun. 2018. DOI: 10.1109/wpt.2018.8639104.

[11] A. Pacini, F. Benassi, D. Masotti, and A. Costanzo, "Design of a RF-to-dc Link for in-body IR-WPT with a Capsule-shaped Rotation-insensitive Receiver," in *2018 IEEE MTT-S International Microwave Symposium (IMS)*, Jun. 2018, pp. 1289–1292. DOI: 10.1109/MWSYM.2018.8439499.

[12] A. Pacini, A. Costanzo, and F. Mastri, "Single or Distributed Inverter for Position Independent Inductive Power Transfer: Comparison of Solutions," in *2nd URSI Atlantic Radio Science Meeting*, May 2018.

[13] A. Pacini, A. Costanzo, S. Aldhaher, and P. D. Mitcheson, "Design of a Position-Independent End-to-End Inductive WPT Link for Industrial Dynamic Systems," in *2017 IEEE MTT-S International Microwave Symposium (IMS)*, Jun. 2017, pp. 1053–1056. DOI: 10.1109/MWSYM.2017.8058774.

[14] A. Pacini, F. Mastri, R. Trevisan, D. Masotti, and A. Costanzo, "Geometry Optimization of Sliding Inductive Links for Position-Independent Wireless Power Transfer," in *2016 IEEE International Microwave Symposium*, Institute of Electrical & Electronics Engineers (IEEE), May 2016. DOI: 10.1109/mwsym.2016.7540073.

[15] A. Pacini, F. Mastri, R. Trevisan, A. Costanzo, and D. Masotti, "Theoretical and Experimental Characterization of Moving Wireless Power Transfer Systems," in *2016 10th European Conference on Antennas and Propagation (EuCAP)*, Institute of Electrical and Electronics Engineers (IEEE), Apr. 2016, pp. 1–4. DOI: 10.1109/eucap.2016.7481913.

[16] A. Pacini, A. Costanzo, and D. Masotti, "A Theoretical and Numerical Approach for Selecting Miniaturized Antenna Topologies on Magneto-Dielectric Substrates," in *2014 44th European Microwave Conference*, Institute of Electrical & Electronics Engineers (IEEE), Oct. 2014. DOI: 10.1109/eumc.2014.6986573.

Patents

[17] A. Pacini and M. J. Kasper, "POWER CONVERTER AND METHOD FOR OPERATING A POWER CONVERTER," DE102024124420A1, Mar. 2026.

[18] A. Pacini and M. J. Kasper, "POWER CONVERTER AND METHOD FOR OPERATING A POWER CONVERTER," US20260066810A1, Mar. 2026.

[19] A. Pacini and M. J. Kasper, "POWER CONVERTER AND METHOD FOR OPERATING A POWER CONVERTER," CN121618841A, Mar. 2026.

[20] A. Pacini, M. J. Kasper, and A. Pevere, "METHOD FOR OPERATING A POWER CONVERTER AND POWER CONVERTER," EP4716079A1, Mar. 2026.

[21] A. Pacini, M. J. Kasper, and A. Pevere, "METHOD FOR OPERATING A POWER CONVERTER AND POWER CONVERTER," CN121710684A, Mar. 2026.

[22] A. Pacini, M. J. Kasper, and A. Pevere, "METHOD FOR OPERATING A POWER CONVERTER AND POWER CONVERTER," US20260088706A1, Mar. 2026.

[23] G. J. Deboy, M. J. Kasper, K. K. Leong, and A. Pacini, "METHOD AND CONTROL CIRCUIT FOR OPERATING A POWER CONVERTER," US12549089B2, Feb. 2026.

[24] A. Pevere, A. Pacini, and M. J. Kasper, "POWER CONVERTER WITH BALANCER INCLUDING FLYING CAPACITOR," US20260039223A1, Feb. 2026.

[25] A. Pevere, A. Pacini, and M. J. Kasper, "POWER CONVERTER WITH BALANCER INCLUDING FLYING CAPACITOR," CN121461741A, Feb. 2026.

[26] A. Pevere, A. Pacini, and M. J. Kasper, "POWER CONVERTER WITH BALANCER INCLUDING FLYING CAPACITOR," DE102025130585A1, Feb. 2026.

[27] A. Pevere, A. Pacini, and M. J. Kasper, "ASYMMETRIC BI-DIRECTIONAL RECTIFIER ARCHITECTURE WITH LEG RECONFIGURATION FOR MULTI-PHASE OPERATION," EP4687272A1, Feb. 2026.

[28] A. Pevere, A. Pacini, and M. J. Kasper, "ASYMMETRIC BI-DIRECTIONAL RECTIFIER ARCHITECTURE WITH LEG RECONFIGURATION FOR MULTI-PHASE OPERATION," US20260039224A1, Feb. 2026.

[29] A. Pevere, A. Pacini, and M. J. Kasper, "ASYMMETRIC BI-DIRECTIONAL RECTIFIER ARCHITECTURE WITH LEG RECONFIGURATION FOR MULTI-PHASE OPERATION," CN121461795A, Feb. 2026.

[30] A. Pacini and M. J. Kasper, "CONTROLLING A POWER CONVERTER," EP4661274A1, Dec. 2025.

[31] A. Pacini and M. J. Kasper, "CONTROLLING A POWER CONVERTER," US2025379507A1, Dec. 2025.

[32] A. Pacini and M. J. Kasper, "CONTROLLING A POWER CONVERTER," CN121098101A, Dec. 2025.

[33] A. Pacini and M. J. Kasper, "METHOD FOR OPERATING A POWER CONVERTER AND POWER CONVERTER," DE102024116035A1, Dec. 2025.

[34] A. Pacini and M. J. Kasper, "METHOD FOR OPERATING A POWER CONVERTER AND POWER CONVERTER," US2025379513A1, Dec. 2025.

[35] A. Pacini and M. J. Kasper, "METHOD FOR OPERATING A POWER CONVERTER AND POWER CONVERTER," CN121098100A, Dec. 2025.

[36] M. J. Kasper and A. Pacini, "METHOD AND CONTROL CIRCUIT FOR OPERATING A POWER CONVERTER ARRANGEMENT AND POWER CONVERTER ARRANGEMENT," DE102023131314A1, May 2025.

[37] M. J. Kasper and A. Pacini, "METHOD AND CONTROL CIRCUIT FOR OPERATING A POWER CONVERTER ARRANGEMENT AND POWER CONVERTER ARRANGEMENT," US2025158507A1, May 2025.

[38] K. K. Leong, G. Deboy, M. J. Kasper, and A. Pacini, "POWER CONVERTER AND POWER CONVERSION METHOD," EP4462664A1, Nov. 2024.

[39] K. K. Leong, G. Deboy, M. J. Kasper, and A. Pacini, "POWER CONVERTER AND POWER CONVERSION METHOD," US2024380316A1, Nov. 2024.

[40] K. K. Leong, G. J. Deboy, M. J. Kasper, and A. Pacini, "POWER CONVERTER AND POWER CONVERSION METHOD," EP4462663A1, Nov. 2024.

[41] G. J. Deboy, M. J. Kasper, K. K. Leong, and A. Pacini, "METHOD AND CONTROL CIRCUIT FOR OPERATING A POWER CONVERTER," EP4422050A1, Aug. 2024.

[42] G. J. Deboy, M. J. Kasper, K. K. Leong, and A. Pacini, "METHOD AND CONTROL CIRCUIT FOR OPERATING A POWER CONVERTER," CN118539707A, Aug. 2024.

Theses

[43] A. Pacini, "Design of Novel Systems for Position Independent Energy and Data Transfer," Ph.D. Thesis, Alma Mater Studiorum - Università di Bologna, Jun. 2019. DOI: 10.6092/unibo/amsdottorato/8930.

[44] A. Pacini, "Approccio teorico e numerico per la selezione di topologie d'antenna miniaturizzate su substrati magneto-dielettrici," Master's Thesis, Alma Mater Studiorum - Università di Bologna, Jun. 2015.

[45] A. Pacini, "Studio di topologie d'antenne su substrati magneto-dielettrici," Bachelor's Thesis, Alma Mater Studiorum - Università di Bologna, Oct. 2012.

Other

Diver CMAS International Certificate ★

AVIS Blood Donor – Silver Medal

Driving Licence EEA – A, B

Villach, AT,
November 15, 2026



Alex Pacini